

FIG. 1

09754406 "010201

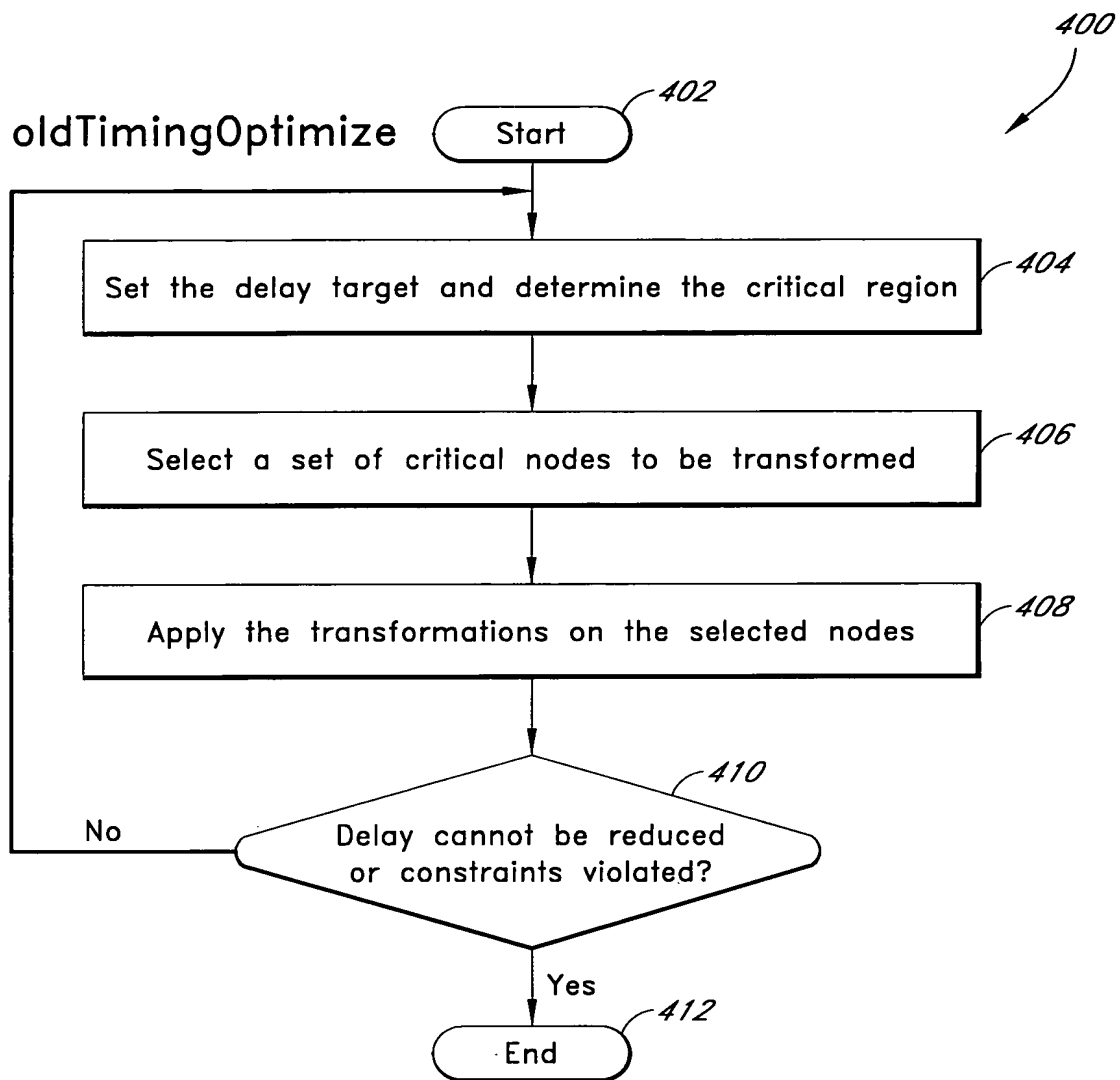


FIG. 2

Local Transformation

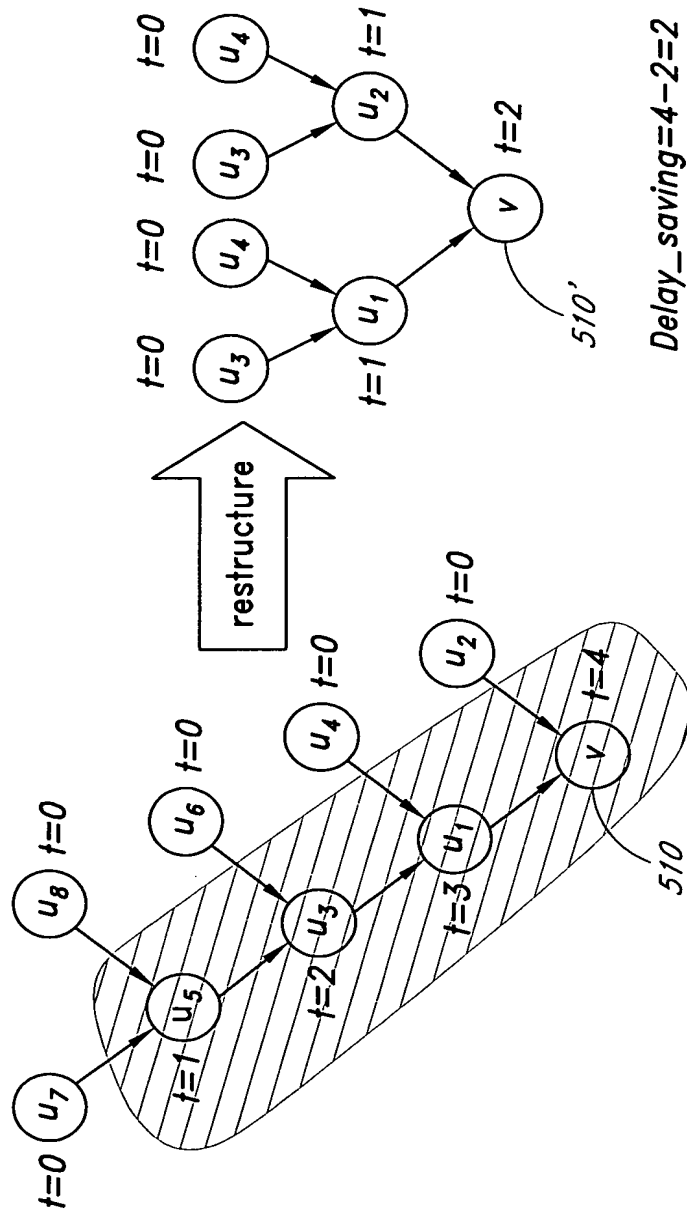


FIG. 3

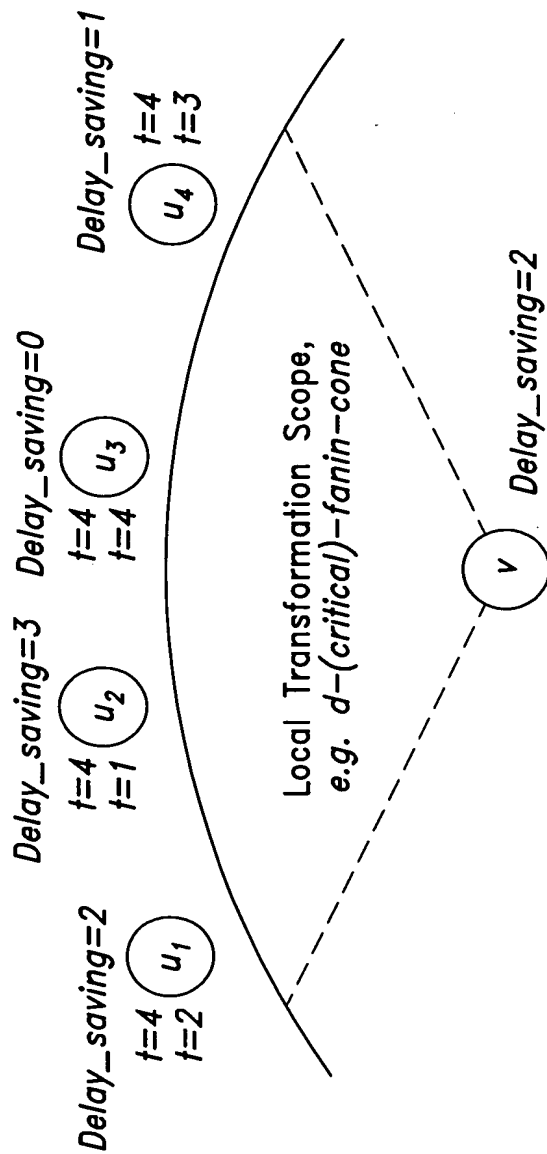


FIG. 4

09754406 " 010201

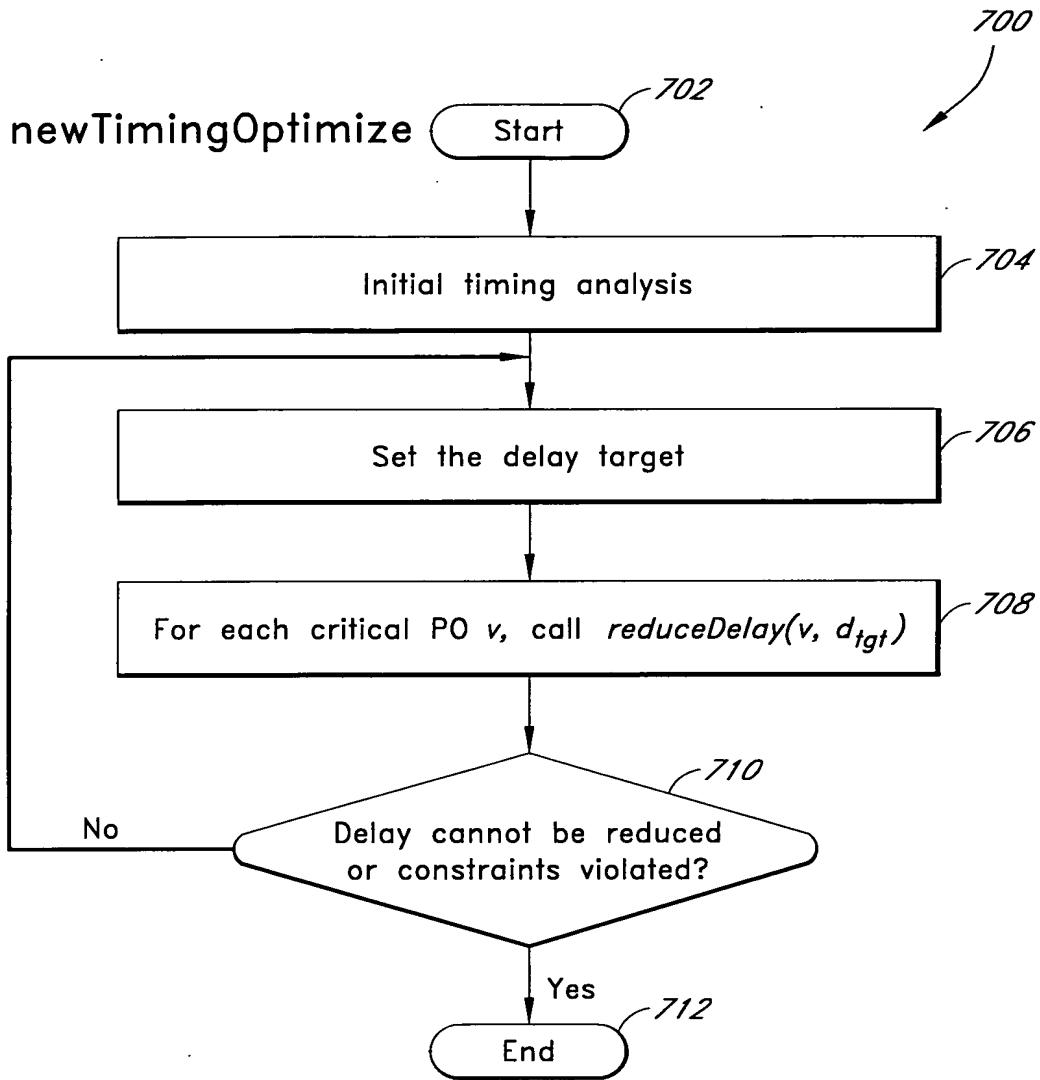


FIG. 5

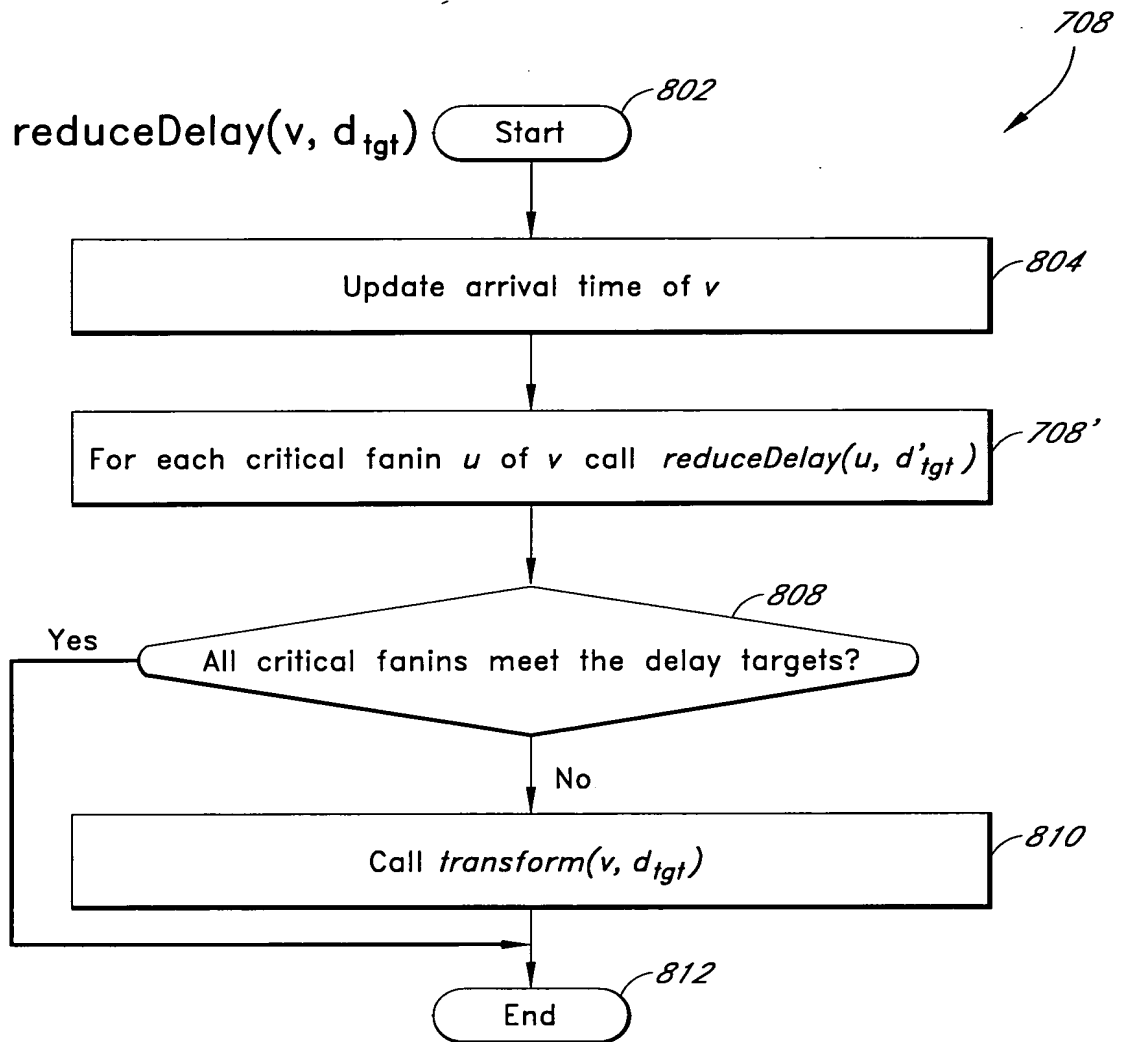


FIG. 6

Example of Recursive Delay Reduction

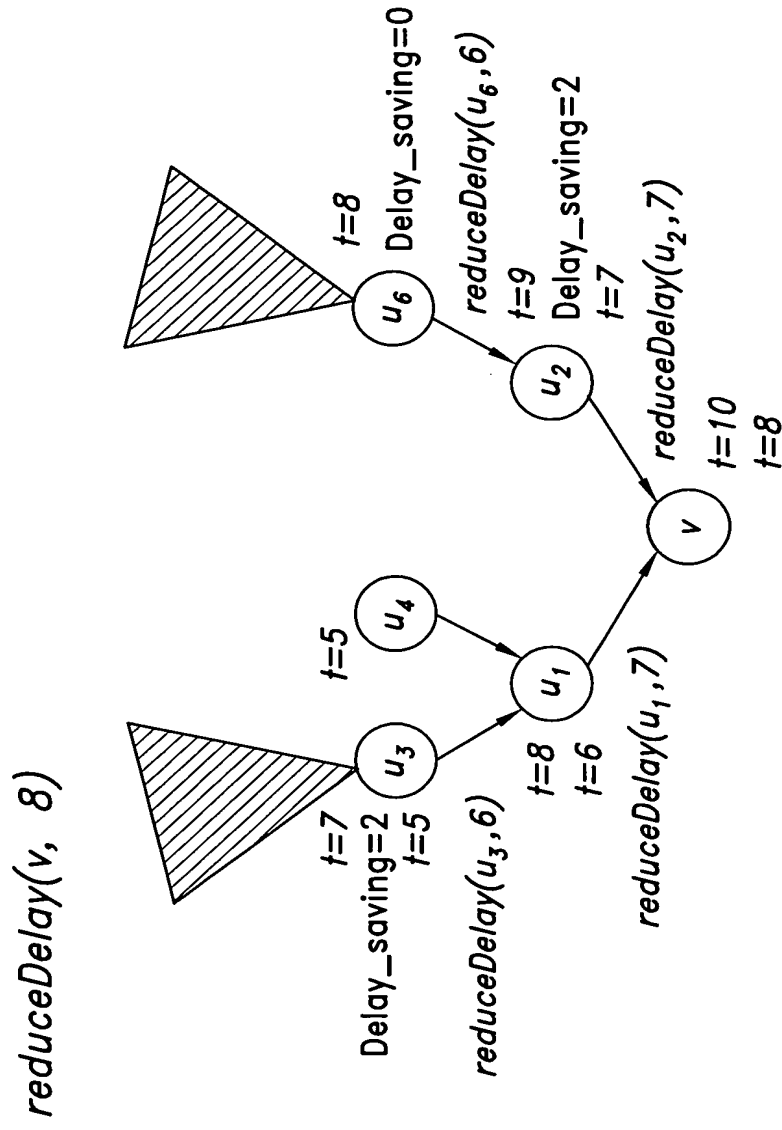
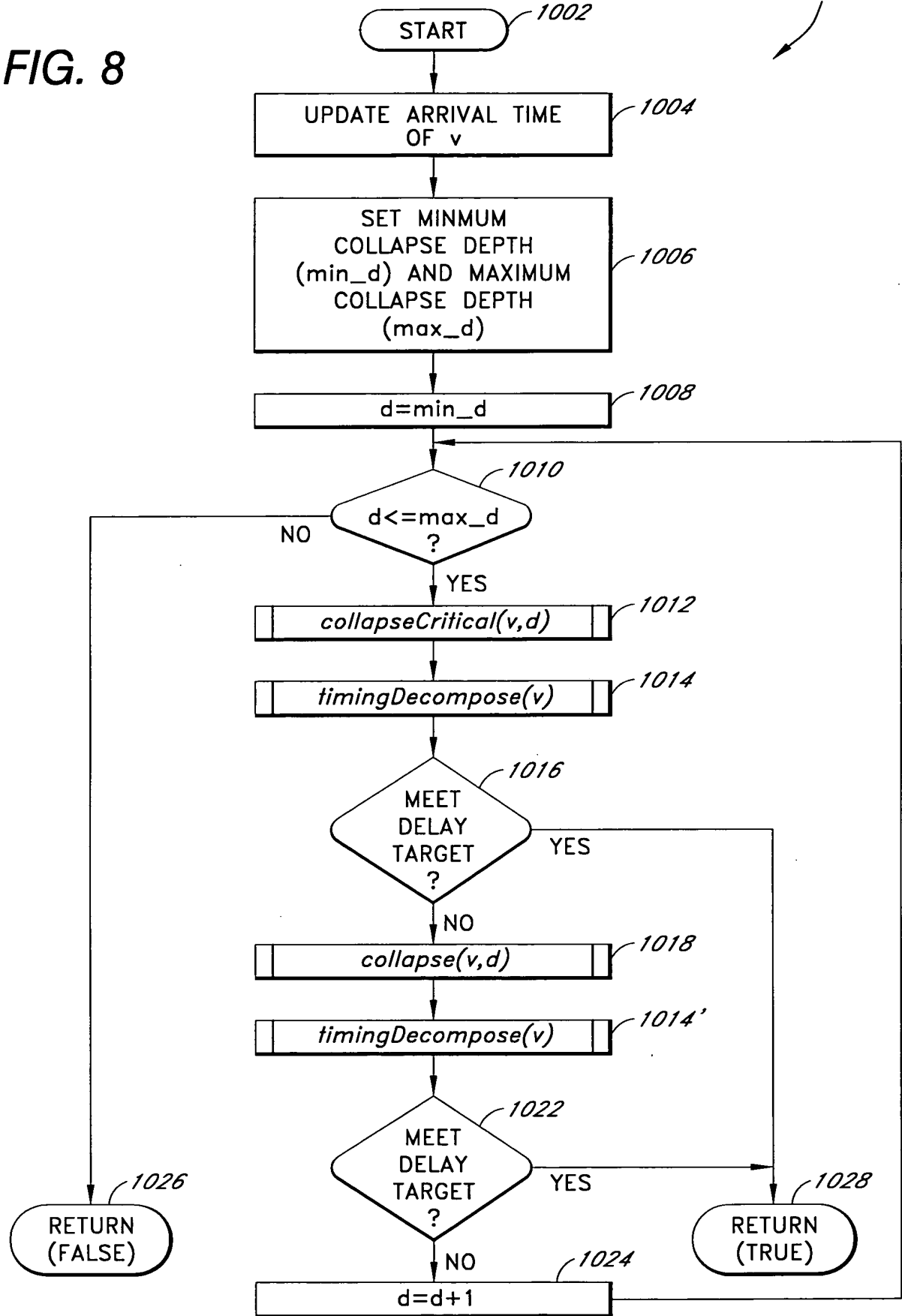


FIG. 7

LOCAL TRANSFORMATION – $\text{transform}(v, d_{\text{tgt}})$

FIG. 8



Impact of Timing Optimization

the impact of timing optimization on the subsequent mapping, clustering and layout design

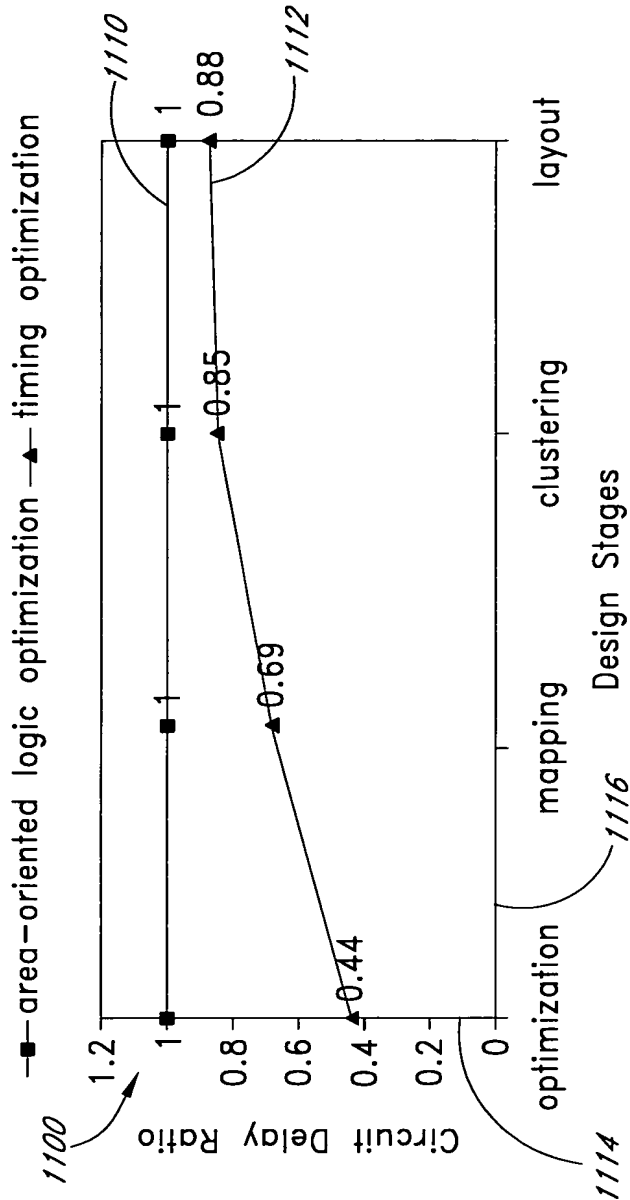


FIG. 9

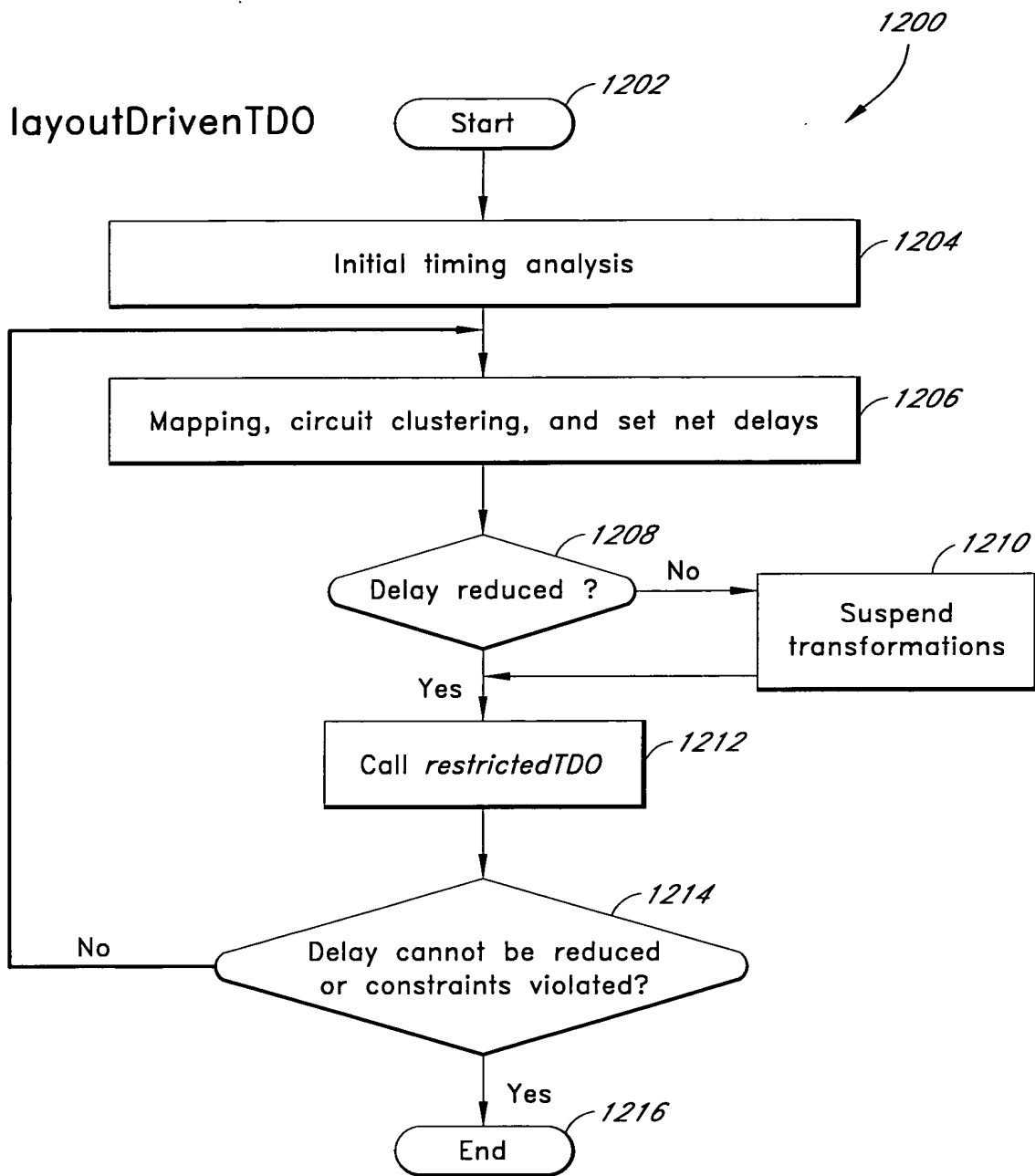


FIG. 10